

PTO/SB/08A (09-06)  
Approved for use through 03/31/2007. OMB 0651-0031  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE  
to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

Sheet	1	of	3
-------	---	----	---

**Complete if Known**

Application Number	10/817,158
Filing Date	April 2, 2004
First Named Inventor	FASSIO, Marcelo Daniel Barú
Art Unit	3762
Examiner Name	MANUEL, George
Attorney Docket Number	99999-003368

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
		CA 2,097,857	06-04-1993	Hoffer et al.		
		WO 2001/60445	08-23-2001	Neurodan A/S		
		WO 2002/13695	02-21-2002	Neopraxis PTY Ltd.		

Examiner  
Signature

Date	
Considered	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.*

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	10/817,158
		Filing Date	April 2, 2004
		First Named Inventor	FASSIO, Marcelo Daniel Baru
		Art Unit	3762
		Examiner Name	MANUEL, George
Sheet 2	of 3	Attorney Docket Number	99999-00 3368

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		ALVAREZ; BiCMOS Technology and Applications; 1993; pp. 317-318; 2nd Edition; Kulwer Academic Publishers.	
		ANDRES et al.; "How to Use Nerve Cuffs to Stimulate..."; Neural Prosthesis; 2001; pp. 139-175.	
		BINKLEY et al.; "A Micropower CMOS, Direct-Coverison..."; IEEE Journal of Solid-State Circuits; March 1998; pp. 344-358; vol. 33; issue 3.	
		DONALDSON et al.; "An Implantable Telemeter For Long-Term..."; Proceedings of the 5th Annual Conference of the International Functional Electrical Stimulation Society.	
		ENZ et al. ; "An Analytical MOS Transistor Model..."; Analog Integrated Circuit Signal Processing; 1995; pp. 83-114; vol. 8.	
		GRAY et al.; "MOS Operational Amplifier Design..."; IEEE Journal of Solid-State Circuits; 1982; pp. 969-982; vol. sc-17; issue 6.	
		GREGORIAN et al.; "Analog MOS Integrated Circuits for Signal Processing"; 1986; pp.131-133; John Wiley & Sons.	
		HAUGLAND et al.; "Slip Information Provided by Nerve Cuff..."; IEEE Transactions on Rehabilitation Engineering; 1994; pp. 29-36; vo. 2; issue 1.	
		JINDAL; "Noise Associated with Distributed Resistance..."; IEEE Transactions on Electron Devices; 1984; pp. 1505-1509; vol 31; issue 10.	
		NICOLLINI et al.; "A 3,3-V 800-n rms Noise..."; IEEE Journal of Solid-State Circuits; 1998; pp. 915-921; vol. 28; issue 8.	

Examiner Signature	Date Considered
--------------------	-----------------

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.  
 1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.  
 This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>		<b>Complete if Known</b>	
		Application Number	10/817,158
		Filing Date	April 2, 2004
		First Named Inventor	FASSIO, Marcelo Daniel Baru
		Art Unit	3762
		Examiner Name	MANUEL, George
Sheet 3	of 3	Attorney Docket Number	99999-003368

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		NIKOLIC et al.; "Instrumentation for ENG and..."; IEEE Transactions on Biomedical Engineering; 1994; pp. 703-706; vol. 41; issue 7.	
		PAPATHANASIOU et al.; "An Implantable CMOS Signal..."; IEEE International Symposium on Circuits and Systems; 2000.	
		SACKINGER et al.; "A General Relationship Between Amplifier Parameters..."; IEEE Transactions on Circuits and Systems; 1991; pp. 1173-1181; vol. 38; issue 10.	
		SACKINGER et al.; "A Versatile Building Block..."; IEEE Journal of Solid-State Circuits; 1987; pp. 287-294; vol. SC-22; issue 2.	
		SILVERIA et al.; "A gm-IsuBD Based Methodology..."; IEEE Journal of Solid-State Circuits; 1996; pp. 1314-1319; vol. 31; issue 9.	
		SINKJAER et al.; "Electroneurographic ENG Signals from ..."; Proceedings of the 5th Annual Conference of the International Functional Electrical Stimulation Society.	
		STRANGE et al.; "Gait Phase Information Provided..."; IEEE Transactions on Biomedical Engineering; 1999; pp. 797-809; vol. 46; issue 7.	
		VITTOZ; "Micropower Techniques, Design of Analog-Digital VLSI Circuits for Telecommunications..."; 1993; pp. 53-67.	

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.